

# Train Control ETCS system ETCS 1

# ETCS System Compatibility Test Description

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History

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**Abrogated documents** 

Name	Version	Date

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## 1. Introduction

#### 1.1 Purpose of the document

The purpose of this document is to define the test scenarios to perform in order to prove the ETCS System Compatibility (ESC) between the trackside ETCS Level 1 with system version 1.Y and the Onboard.

The tests scenarios describes more in detail each "high level" scenarios defined in the ESC test plan [1].

The success of these test scenarios shall prove the technical compatibility between ETCS On-board and the Trackside part ETCS of the CCS subsystems within the ETCS1 with system version 1.Y area on Infrabel conventional network.

The technical specification for interoperability used inside an ETCS1 with system version 1.Y area on Infrabel network is the set of specifications 1, B2(Cfr [2] and [3]).

These test scenarios for ETCS system compatibility do not cover all design rules used in an ETCS1 area. If required, Infrabel can provide additional operational test scenarios performed during the verification that the trackside subsystem complies with the requirement of the TSI.

In case of doubt concerning the ESC of the board with the trackside, the railway undertaking shall take the required action with his supplier and inform Infrabel.

#### 1.2 Basic documents

Ref.	Title	Owner
[1]	PSI (TC,ETCSsys,z) ESC TST PLN 1.2	Infrabel

#### 1.3 Reference documents

Ref.	Title	Owner
[2]	Commission Decision (EU) 2012/88/EU of 25 January 2012	UE
[3]	Commission Decision (EU) 2012/696/EU of 6 November 2012	UE
[4]	PSI(TP,ETCSsys.L1LS.z) ESC TST DSC	Infrabel
[5]	PSI(TC,ETCSsys.L2,z) ESC TST DSC	Infrabel

#### 1.4 Annexes

Ref.	Title	0	wner
[6]	None		

#### 1.5 **Scope**

This document is applicable for all trains would run under the protection of ETCS level 1 in an ETCS1 with system version 1.Y area on the Infrabel conventional network.

#### 1.6 **Definitions, symbols and abbreviations**

CCS	Control Command System
DMI	Driver Machine Interface
ESC	ETCS System Compatibility
ETCS	European Train Control System
IBG	Infill Balise Group
LS	Limited Supervision
NR	Not Relevant
SBG	Signal Balise Group

TSI Technical Specification for Interoperability



## 1.7 Known imperfections

No test cases are defined for transitions from and to TVM430 in this version of the document.

# 2. On-board Equipment

Out of scope of railway manager Infrabel.



## 3. Functionalities

The tested functionalities are described in the table here under:

Test scenario (ref ESC TST PLN [1])	Tested functionality
ESC_L1FS_1	Train categories
ESC_L1FS_2	IREPOS
ESC_L1FS_3	Stop on two consecutive sections
ESC_L1FS_4	Crossing closed non-permissive signal without override
ESC_L1FS_5	Crossing closed non-permissive signal with override
ESC_L1FS_6	Crossing a closed permissive signal
ESC_L1FS_7	CR819
ESC_L1FS_8	CR1120
ESC_TR_1	ETCS 1 FS >> ETCS 1 LS (out of scope of this document)
ESC_TR_3	ETCS 1 LS >> ETCS 1 FS (out of scope of this document)
ESC_TR_5	ETCS 1FS >> TVM430
ESC_TR_7	TVM430 >> ETCS 1FS
ESC_TR_9	ETCS1 FS >> ETCS 2 FS (out of scope of this document)
ESC_TR_10	ETCS 2 FS >> ETCS 1 FS (out of scope of this document)
ESC_TR_12	ETCS 1 FS >> STM TBL1+
ESC_TR_15	STM TBL1+ >> ETCS 1 FS

The document will only describe the sequences to perform the scenarios but not all the actions to prepare the execution of the test scenarios.

Transitions to and from ETCS1 Limited supervision (ESC\_TR\_1 and ESC\_TR\_3) are covered in the ESC test DSC for ETCS1 LS program (cf. [4]).

Transitions to and from ETCS Level 2 (ESC\_TR\_9 and ESC\_TR\_10) are covered in the ESC test DSC for ETCS2 program (cf. [5]).



# 4. Test scenarios

## 4.1 Test ESC\_L1FS\_1: Train categories

## 4.1.1 Description

ID	Date			Location / Line			
ESC_L2FS_1 part1	<dd mm="" yyyy=""></dd>			<line></line>			
Description	Functionalities tested	Functionalities tested :					
	<ul> <li>Tests ESC_</li> </ul>	<ul> <li>Tests ESC_L1FS_1: train categories</li> </ul>					
	This test does not no	This test does not need to be repeated with all possible categories. The train categories should not be the one associated					
	with the lowest speed profile.						
Signal passed							
Name Trackside datafile in service							
Panel P1 at bk bk of the sp	peed panel> is at the be	ginning of a train category speed restricted area.					
Signal S1: <signal name=""> is</signal>	s closed: Signal S1 is up	wards panel P1.					
Test Scenarios							
Starting condition Track 1: track <track number=""/>							
	Train A						
		Level 1 mode FS					
		rear of signal S1					
	_	ory <freight freight="" g="" p="" passenger=""> is selected of</freight>					
		ations are filled in before performing the test sc	enarios				
Sequences of the test scen-	ario						
Step Step description		Description of what to be tested	Statement	Comment			
1 Signaller opens s	ignal S1.	Train A receives an MA with a packet 27 including	Pass / Fail				
		train category speed restriction.					
	passes the signal S1	The speed is limited at <train category="" max<="" td=""><td>Pass / Fail</td><td></td><td></td></train>	Pass / Fail				
and panel P1.		speed> km/h at panel P1.					
Test scenario finished							



4.1.2 Scenario diagram	
	None
Final State	Train in level1 FS beyond panel P1



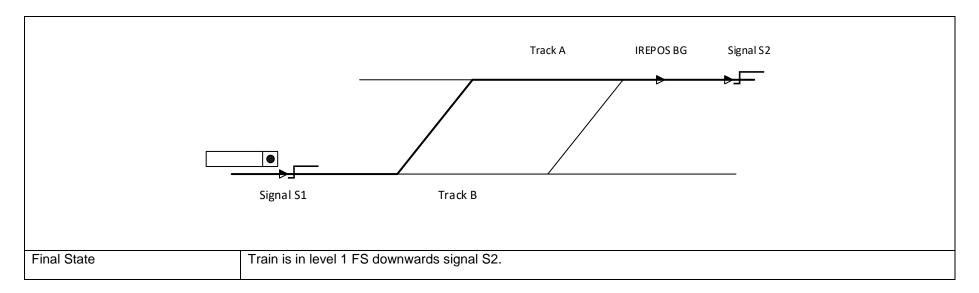
## 4.2 Test ESC\_L1FS\_2: IREPOS

## 4.2.1 Description

ID		Date			Location / Line		
ESC_L1F	FS_2	<dd mm="" yyyy=""></dd>			<line></line>		
Description	on	Functionalities tested	:				
		<ul><li>Test ESC_L3</li></ul>	1FS_2: IREPOS				
		The section between signals S1 and S2 is a regrouped section. The itinerary passing by track <track numbers<="" td=""/>					
		(track A) is the longest itinerary of the regrouped itineraries.					
	The IREPOS BG ( <nid_c nid_bg="">) sends IREPOS information <distance bg="" signal="">m upwards signal S1.</distance></nid_c>						
Signal pa	assed						
Name				Trackside datafile in service			
Signal S1: <signal name=""> is open</signal>							
	2: <signal name=""> is</signal>	closed					
Test Sce							
Starting of	condition	Train is upwards sign	al S1 in ETCS 1 FS.				
		The route is set between	veen S1 and S2 passing by track A.				
			ations are filled in before performing the test	scenarios			
•	es of the test scenar	io					
Step	Step description		Description of what to be tested		omment		
1	Train passes signa		The length of the current section is extended	Pass / Fail			
	Train receives IREPOS information		by <difference between="" distance="" of="" regrouped<="" td=""><td></td><td></td></difference>				
	when passing IREPOS BG.		routes>m.				
2	Signaller opens signal S2.	signal S2 then train	The MA is extended downwards signal S2	Pass / Fail			
Test scer	nario finished						



## 4.2.2 Scenario diagram





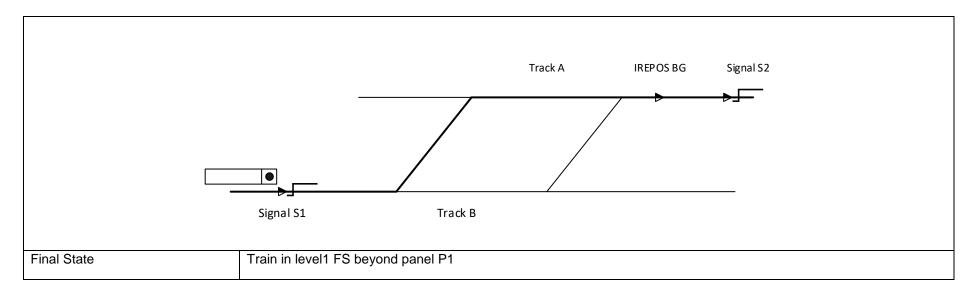
## 4.3 Test ESC\_L1FS\_2\_part 2: IREPOS

## 4.3.1 Description

ID		Date			Location / Line			
ESC_L1I	FS_2_part2	<dd mm="" yyyy=""></dd>			<line></line>			
Descripti	ion	Functionalities tested	Functionalities tested :					
		<ul> <li>Test ESC_L:</li> </ul>	Test ESC_L1FS_2: IREPOS					
		The section betwee	The section between signals S1 and S2 is a regrouped section. The itinerary passing by track <track number=""/> (track					
		A) is the longest itir	A) is the longest itinerary of the regrouped itineraries.					
		The IREPOS BG ( <nid_c nid_bg="">) sends IREPOS information <distance bg="" signal="">m upwards signal S1.</distance></nid_c>						
Signal pa	assed							
Name				Trackside datafile in s	ervice			
Signal S1: <signal name=""> is open</signal>								
	2: <signal name=""> is</signal>	closed						
Test Sce								
Starting of	condition	Train is upwards sign	al S1 in ETCS 1 FS.					
		The route is set betw	een S1 and S2 passing by track A.					
		Be sure all authoris	ations are filled in before performing the test	scenarios				
Sequenc	es of the test scenar	rio						
Step	Step description		Description of what to be tested	Statement Com	ment			
1		al S1 while the signal	The MA has the length of the shortest itinerary	Pass / Fail				
	S2 is closed.		to the signal S2, and the IREPOS BG is linked with identifier "UNKNOWN".					
2	Before the train r	eaches the IREPOS	The MA is extended beyond the signal S2	Pass / Fail				
	BG, the signal S2 i	s open.	(infill information is accepted) and the length					
	•	REPOS BG receiving	of the current section is extended by					
	IREPOS information	on.	<difference between="" distance="" of="" regrouped<="" td=""><td></td><td></td></difference>					
			routes>m.					
<b>T</b> (								
l est scel	nario finished							



## 4.3.2 Scenario diagram





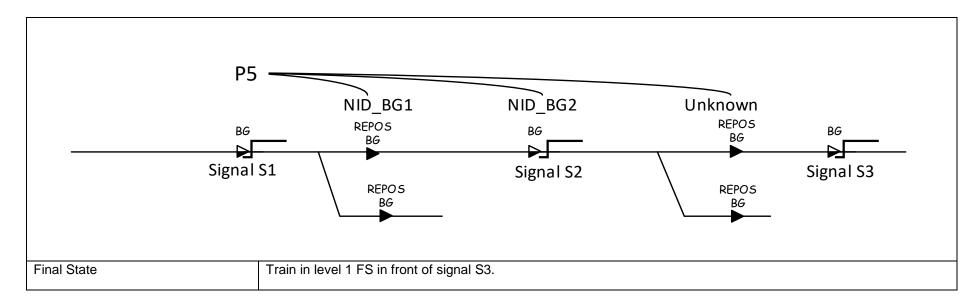
## 4.4 Test ESC\_L1FS\_3: REPOS on two consecutive sections

## 4.4.1 Description

ID		Date				Location / Line
ESC_L1FS_3		<dd mm="" yyyy=""></dd>				<line></line>
Description		Functionalities tested	:			
	<ul> <li>Test ESC_L1FS_3: REPOS on two consecutive sections</li> </ul>					
		At a portion of track where the routes are unified in 2 consecutive sections, BG sending the P16 are chained with the known				
	NID_BG on the first section and with the unknown NID_BG on the second section.					
Signal passed	d					
Name Trackside datafile in service						
S1: <signal r<="" td=""><td>name&gt; is open</td><td></td><td></td><td></td><td></td><td></td></signal>	name> is open					
S2: <signal name=""> is open</signal>						
S3: <signal r<="" td=""><td>name&gt; is closed</td><td></td><td></td><td></td><td></td><td></td></signal>	name> is closed					
Test Scenario	S					
Starting condi	ition	Train in level 1 FS up	wards signal S1.			
		Be sure all authorisa	ations are filled in before	e performing the test	scenarios	
Sequences of	the test scenar	io				
Step Ste	Step Step description Description of what to be		Description of what to be	e tested	Statement	Comment
1 Tra	Train passes signal S1 and goes to No linking error when		n passing the two	Pass / Fail		
sig	nal 3		repositioning BG's.			
Test scenario	finished					



## 4.4.2 Scenario diagram





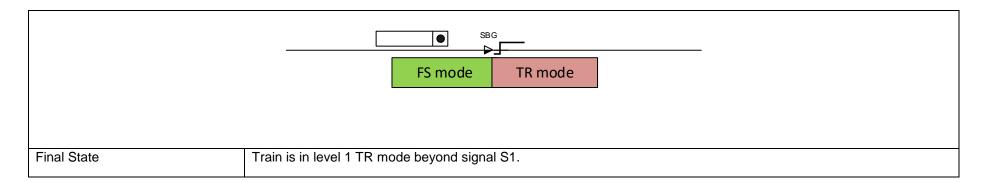
## 4.5 **Test ESC\_L1FS\_4: Crossing closed non-permissive signal without override**

## 4.5.1 Description

ID	Date			Location / Line		
ESC_L1FS_4	<dd mm="" yyyy=""></dd>		<line></line>			
Description	Functionalities tested	d :				
	Test ESC_L1	Test ESC_L1FS_4: Crossing closed non-permissive signal without override				
Signal passed						
Name		Tracksi	de datafile in service			
S1: <signal name<="" td=""><td>&gt; is a closed-controlled main sto</td><td>p signal.</td><td></td><td></td></signal>	> is a closed-controlled main sto	p signal.				
Test Scenarios						
Starting condition	Train is :					
	in level 1 FS	mode				
	at standstill u	ıpwards signal S1.				
	Be sure all authoris	ations are filled in before perfori	ning the test scenarios.			
Sequences of the	test scenario					
Step Step de	scription	Description of what to be tested	Statement	Comment		
override.	sses signal S1 without activation of the The train should be at low speed when he signal.	Train is tripped and emergency brakes a	re applied. Pass / Fail			
2			Pass / Fail			
Test scenario finis	hed					



## 4.5.2 Scenario diagram





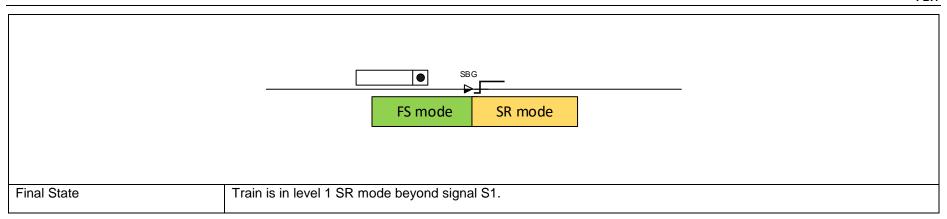
## 4.6 **Test ESC\_L1FS\_5: Crossing closed non-permissive signal with override**

## 4.6.1 Description

ID	Date	Date			Location / Line	
ESC_L1FS_5	<dd mm="" yyyy=""></dd>	<dd mm="" yyyy=""></dd>			<line></line>	
Description	Functionalities tested	l:				
	Test ESC_L1F	Test ESC_L1FS_4: Crossing closed non-permissive signal with override				
Signal passed						
Name	Trackside datafile in service					
S1: <signal name=""> is a closed-controlled main stop signal.</signal>						
S2: <signal name<="" td=""><td>&gt; is an opend-main stop signal e</td><td>quipped with an IBG</td><td></td><td></td><td></td></signal>	> is an opend-main stop signal e	quipped with an IBG				
Test Scenarios						
Starting condition	Starting condition Train is:					
	in level 1 FS	mode				
	at standstill u	upwards signal S1.				
	Be sure all authoris	ations are filled in before	performing the test	scenarios.		
Sequences of the	test scenario					
Step Step de	scription	Description of what to be tested		Statement	Comment	
	sses signal S1 at low speed after of the override.	-		Pass / Fail		
Test scenario finis	hed					

## 4.6.2 Scenario diagram







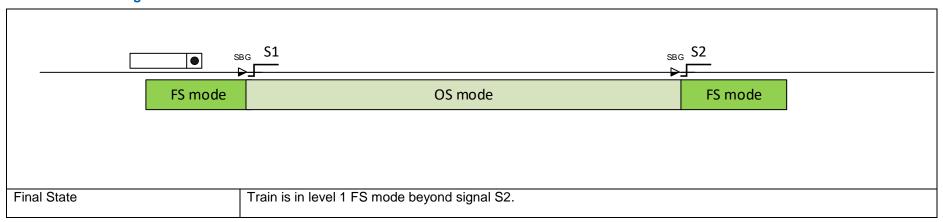
## 4.7 Test ESC\_L1FS\_6: Crossing a closed permissive signal

## 4.7.1 Description

ID		Date				Location / Line
ESC_L1F	FS_6	<dd mm="" yyyy=""></dd>				<line></line>
Description	on	Functionalities tested	:			
		Test ESC_L1FS_6: Crossing a closed permissive signal				
Signal pa	assed					
Name Trackside datafile in service						
<b>S1</b> : < <b>S</b> ig	nal name> is a close	ed-non controlled main	stop signal.			
S2: <sig< td=""><td>nal name&gt; is an ope</td><td>en main stop signal. Ed</td><td>uipped with an IBG</td><td></td><td></td><td></td></sig<>	nal name> is an ope	en main stop signal. Ed	uipped with an IBG			
Test Sce	narios					
Starting condition  Train is:  in level 1 FS mode  at standstill upwards sign  Be sure all authorisations are f			pwards signal S1.	e performing the te	est scenarios.	
Step	es of the test scenar Step description	10	Description of what to be	a tastad	Statement	Comment
1		S1 at low speed without de.	Train changes to OS mode.	Coleu	Pass / Fail	Comment
2	The train passes IBG of	train passes IBG of S2 Train rejects the infill informat		ion.	Pass / Fail	
3	Train continues and pa	sses S2	Train changes to FS mode		Pass / Fail	
Test scer	nario finished					



## 4.7.2 Scenario diagram





## 4.8 **Test ESC\_L1FS\_7 : CR819**

## 4.8.1 Description

ID		Date			Location / Line		
ESC_L1F	-S_7	<dd mm="" yyyy=""></dd>			<line></line>		
Description	on	Functionalities tested	:		•		
- ESC_L1FS_7 : CR819							
	If the balises are duplicated within a balise group and a balise is not read or not decoded correctly but the duplicated ba						
	is, then the message shall not be rejected and no linking reaction shall be applied.						
Signal pa	assed						
Name			Trackside datafile in	service			
Test Sce	narios						
Starting of	condition	Train is in level 1 mod	de FS upwards a fixed balise group sending a te	ext message.			
		A cover is installed or	on the first balise of the fixed BG (N_PIG = 0).				
		Be sure all authoris	ations are filled in before performing the tes	t scenarios			
Sequence	es of the test scenar	rio					
Step	Step description		Description of what to be tested	Statement	Comment		
1	·		No linking reaction occurs and the text	Pass / Fail			
			message <text bg="" by="" send="" the=""> is displayed</text>				
			on the DMI.				
2				Pass / Fail			
Test scer	nario finished						



4.0.2 Scellar lo diadral	4.8.2	Scenario	diagram
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	none
Final State	Train in level 1 FS beyond fixed BG



## 4.9 **Test ESC\_L1FS\_8: CR1120**

## 4.9.1 Description

ID	Date				Location / Line			
ESC_L1FS_8	<dd mm="" yyyy=""></dd>				<line></line>			
Description	Functionalities tested	Functionalities tested :						
	- ESC_L1FS_8	- ESC_L1FS_8: CR1120						
	In a transition from level STM TBL1+ to ETCS 1 FS, the train receives a transition announcement applicable to the next signal.							
	This signal is equipped with an IBG. The train crosses the IBG.							
	Expected result: pack	cets 5, 12, 21, 27 received	d at the IBG are not acc	epted without a	ccepting packet 136.			
Signal passed								
Name Trackside datafile in service								
S1: <signal a="" name="" of="" signal="" transition=""> is open presenting Y aspect.</signal>								
S2: <signal a="" name="" of="" signal="" transition=""> is closed.</signal>								
Test Scenarios								
Starting condition Train is in level STM upwards signal S1.								
	Train is upwards signal S1							
	Be sure all authorisa	ations are filled in befor	e performing the test	scenarios				
Sequences of the test scena	rio							
Step Step description		Description of what to be	e tested	Statement	Comment			
1 Train passes signa	al S1	Train receives an MA with an announcem		Pass / Fail				
		of transition to level 1.						
2 Signaller opens s	signal S2 before train	Train receives infill in	formation and reject	Pass / Fail				
passes the IBG of	signal S2							
3 Train passes signa	al S2	Train changes to level 1	FS mode.	Pass / Fail				
Test scenario finished								



4.9.2	Scenario	di	iag	ram
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	None
Final State	Train in level 1 mode FS downwards signal S2.



## 4.10 Test ESC\_TR\_5 : Transition Level 1 FS to TVM430

Test case to be defined.

## 4.10.1 Description

ID		Date				Location / Line		
ESC_TR_	_5	<dd mm="" yyyy=""></dd>				<line></line>		
Description	on	Functionalities tested	d :			•		
		- ESC_TR_5: Transition Level 1 FS to TVM430.						
Signal pas	ssed							
Name				Trackside datafile i	n service			
Test Scer	narios							
Starting c	ondition							
		Be sure all authoris	sations are filled in before	performing the te	st scenarios			
Sequence	es of the test scena	rio						
Step	Step description		Description of what to be	tested	Statement	Comment		
1	To be defined				Pass / Fail			
2					Pass / Fail			
3					Pass / Fail			
Test scen	ario finished					·		



Final State

4.10.2 Scenario	diagram			
		None		



## 4.11 Test ESC\_TR\_7: Transition TVM430 to Level 1 FS

Test case to be defined.

## 4.11.1 Description

ID		Date				Location / Line			
ESC_TR	R_7	<dd mm="" yyyy=""></dd>				<line></line>			
Descripti	ion	Functionalities tested	l:			•			
		- ESC_TR_7: Transition TVM to ETCS1 FS							
Signal pa	assed								
Name				Trackside data	afile in service				
Test Sce	enarios								
Starting	condition								
		Be sure all authoris	ations are filled in before	re performing t	he test scenarios				
Sequenc	ces of the test scenar	rio							
Step	Step description		Description of what to b	e tested	Statement	Comment			
1 To be defined				Pass / Fail					
2					Pass / Fail				
3					Pass / Fail				
Test sce	nario finished					-			



4.11.2	Scenario	diagram
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	None	
Final State		



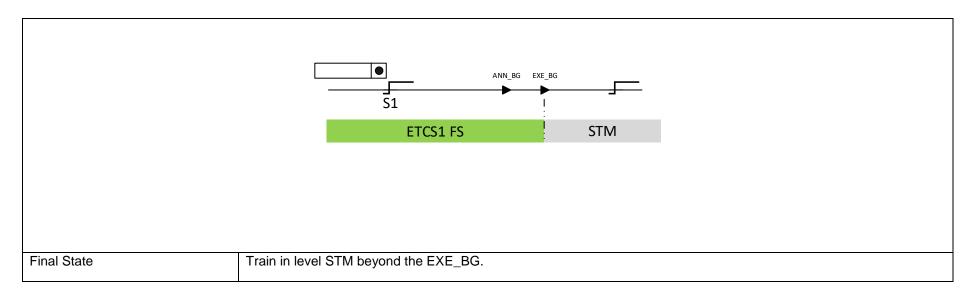
## 4.12 Test ESC\_TR\_12: Transition ETCS1 FS to STM TBL1+

## 4.12.1 Description

ID		Date				Location / Line				
ESC_TR	R_12	<dd mm="" yyyy=""></dd>				<line></line>				
Descripti	ion	Functionalities tested :								
		- ESC_TR_12 : Transition ETCS1 FS to STM TBL1+								
			Remark: if test case is executed in CVT track, a text message "=>S*" is displayed between from reception of the transition up							
		to the first TBL1+ sign	nal. An acknowledgment o	of the text message is	required.					
Signal pa	aaaad									
Name	a55eu			Trackside datafile in	convice					
	anal namas is anan	It is the last ETCS1 FS	Coignal	TrackSide dataille in s	Service					
31. 3	griai riairie> is operi.	IL IS THE IAST ETCST FO	o signai.							
Test Sce	narios									
	condition	Train is in level 1 mod	de FS upwards signal S1.							
Ctarting	Corrainori	1141110 11110001 1 11100	5 To aprivate signar of the							
		Be sure all authoris	ations are filled in befor	e performing the test	scenarios					
Sequenc	ces of the test scenar									
Step	Step description		Description of what to be	e tested	Statement	Comment				
1	Train passes sign	al S1 and the level	Train receives a transiti	on execution to level	Pass / Fail					
	transition announce	ement fixed BG at the	STM. A level transition announcement is							
	reference speed of	f the line.	displayed on the DMI.							
			Acknowledgement is	•						
			he announcement.							
2	Driver acknowledge the transition and Train switches to leve			TM.	Pass / Fail					
	•	ne level transition								
	execution BG.									
Test sce	nario finished									



## 4.12.2 Scenario diagram





## 4.13 Test ESC\_TR\_15: Transition STM TBL1+ to ETCS1 FS

## 4.13.1 Description

ID	Date						Location / Line	
ESC_TR_15	<dd mr<="" td=""><td colspan="4"><dd mm="" yyyy=""></dd></td><td><line></line></td></dd>	<dd mm="" yyyy=""></dd>				<line></line>		
Description	Function	Functionalities tested :						
	-	- ESC_TR_15 Transition STM TBL1+ to ETCS1 FS						
	Remark: the transition STM TBL1+ to ETCS1 FS with an announcement of transition at the first signal is tested in to ESC_L1FS_8: CR1120.							
Signal passed								
Name				Trackside datafile in	service			
S1: <signal name<="" td=""><td>&gt; is open. It is the</td><td>first ETCS1 signa</td><td>al</td><td></td><td></td><td></td><td></td></signal>	> is open. It is the	first ETCS1 signa	al					
S2: <signal name<="" td=""><td>&gt; is open</td><td></td><td></td><td></td><td></td><td></td><td></td></signal>	> is open							
Test Scenarios								
Starting condition	Train is	s in level STM TE	BL1+ in rear of signal S1					
	Be sur	re all authorisati	ons are filled in before	e performing the test	scenarios			
Sequences of the	test scenario							
Step Step de	scription	C	Description of what to be	tested	Statement	Commer	nt	
1 Train pa	Train passes signal S1 Train switches to level 1		FS mode.	Pass / Fail				
2 Drivers	2 Drivers acknowledges transition Train remains in level 1		rain remains in level 1 F	S mode.	Pass / Fail			
Test scenario finis	Test scenario finished							



## 4.13.2 Scenario diagram

